

<b>FRIDAY</b>	09:00-09:45	VLSI DESIGN(EEC-703) ALQUAMAR KAUSAR	09:00-09:45	Free Period
	09:45-10:30	DATA COMMUNICATION THEORY ARCHANA SHARMA	09:45-10:30	Free Period
	10:30-11:15	OPTICAL COMMUNICATION THEORY PRATYUSH PUSHKAR	10:30-11:15	Free Period
	11:15-12:00	SATELLITE COMMUNICATION THEORY ALQUAMAR KAUSAR	11:15-12:00	Free Period
	12:00-12:45	QUALITY MANAGEMENT THEORY VIKSIT GAUR	12:00-12:45	Free Period
<b>SATURDAY</b>	09:00-09:45	VLSI DESIGN(EEC-703) ALQUAMAR KAUSAR	09:00-09:45	Free Period
	09:45-10:30	DATA COMMUNICATION THEORY ARCHANA SHARMA	09:45-10:30	Free Period
	10:30-11:15	OPTICAL COMMUNICATION THEORY PRATYUSH PUSHKAR	10:30-11:15	Free Period
	11:15-12:00	SATELLITE COMMUNICATION THEORY ALQUAMAR KAUSAR	11:15-12:00	Free Period
	12:00-12:45	QUALITY MANAGEMENT THEORY VIKSIT GAUR	12:00-12:45	Free Period
<b>SUNDAY</b>	09:00-09:45	VLSI DESIGN(EEC-703) ALQUAMAR KAUSAR	09:00-09:45	Free Period
	09:45-10:30	DATA COMMUNICATION THEORY ARCHANA SHARMA	09:45-10:30	Free Period
	10:30-11:15	OPTICAL COMMUNICATION THEORY PRATYUSH PUSHKAR	10:30-11:15	Free Period
	11:15-12:00	SATELLITE COMMUNICATION THEORY ALQUAMAR KAUSAR	11:15-12:00	Free Period
	12:00-12:45	QUALITY MANAGEMENT THEORY VIKSIT GAUR	12:00-12:45	Free Period
	12:45-13:15	30 MINS LUNCH BREAK	12:45-13:15	30 MINS LUNCH BREAK
	13:15-14:00	GATE GATE VIKSIT GAUR	13:15-14:00	Free Period
	14:00-14:45	SATELLITE COMMUNICATION TUTORIALS ALQUAMAR KAUSAR	14:00-14:45	Free Period
	14:45-15:30	PROJECT LAB	14:45-15:30	Free Period
	15:30-16:15	PROJECT LAB	15:30-16:15	Free Period
	16:15-16:30	Free Period	16:15-16:30	Free Period

HOD

~~Signature~~  
26/05/18

Academic cell

