

**Template for Site: SITM-SAROJ INSTITUTE OF TECHNOLOGY AND MANAGEMENT , Batch: B.Tech EC 2012-16 ,
 Program: Electronics and Communication Engineering , Session: SEMESTER 7 , Class: CLASS - 7 - 1**

DAY/TIME	1	2	3	4	5	6	7	8	9	10	11
MONDAY	09:00-09:45 VLSI DESIGN/ECC-703 THEORY ALOUAMAR KAUSAR	09:45-10:30 TEST TEST	10:30-11:15 OPTICAL COMMUNICATION THEORY PRAITYUSH PUSHKAR	11:15-12:00 SATELLITE COMMUNICATION THEORY ALOUAMAR KAUSAR	12:00-12:45 QUALITY MANAGEMENT THEORY VIKSIT GAUR	12:45-13:15 LUNCH BREAK 30 MINS	13:15-14:00 GATE GATE SATISH CHANDRA KUSHWAHA	14:00-14:45 DATA COMMUNICATION NETWORK THEORY	14:45-15:30 MICROWAVE & FIBRE OPT LAB PRAITYUSH PUSHKAR	15:30-16:15 MICROWAVE & FIBRE OPT LAB PRAITYUSH PUSHKAR	16:15-16:30 Free Period
TUESDAY	09:00-09:45 VLSI DESIGN/ECC-703 THEORY ALOUAMAR KAUSAR	09:45-10:30 DATA COMMUNICATION NETWORK THEORY ARCHANA SHARMA	10:30-11:15 OPTICAL COMMUNICATION THEORY PRAITYUSH PUSHKAR	11:15-12:00 SATELLITE COMMUNICATION THEORY ALOUAMAR KAUSAR	12:00-12:45 QUALITY MANAGEMENT THEORY VIKSIT GAUR	12:45-13:15 LUNCH BREAK 30 MINS	13:15-14:00 GATE GATE SATISH CHANDRA KUSHWAHA	14:00-14:45 QUALITY MANAGEMENT THEORY VIKSIT GAUR	14:45-15:30 ECD LAB LAB ALOUAMAR KAUSAR	15:30-16:15 ECD LAB LAB ALOUAMAR KAUSAR	16:15-16:30 Free Period
WEDNESDAY	09:00-09:45 VLSI DESIGN/ECC-703 THEORY ALOUAMAR KAUSAR	09:45-10:30 DATA COMMUNICATION NETWORK THEORY ARCHANA SHARMA	10:30-11:15 OPTICAL COMMUNICATION THEORY PRAITYUSH PUSHKAR	11:15-12:00 SATELLITE COMMUNICATION THEORY ALOUAMAR KAUSAR	12:00-12:45 QUALITY MANAGEMENT THEORY VIKSIT GAUR	12:45-13:15 LUNCH BREAK 30 MINS	13:15-14:00 GATE GATE SATISH CHANDRA KUSHWAHA	14:00-14:45 OPTICAL COMMUNICATION NETWORK THEORY PRAITYUSH PUSHKAR	14:45-15:30 Free Period	15:30-16:15 Free Period	16:15-16:30 Free Period
THURSDAY	09:00-09:45 VLSI DESIGN/ECC-703 THEORY ALOUAMAR KAUSAR	09:45-10:30 DATA COMMUNICATION NETWORK THEORY PRAITYUSH PUSHKAR	10:30-11:15 OPTICAL COMMUNICATION THEORY PRAITYUSH PUSHKAR	11:15-12:00 SATELLITE COMMUNICATION THEORY ALOUAMAR KAUSAR	12:00-12:45 QUALITY MANAGEMENT THEORY VIKSIT GAUR	12:45-13:15 LUNCH BREAK 30 MINS	13:15-14:00 GATE GATE PRAITYUSH PUSHKAR	14:00-14:45 VLSI DESIGN/ECC-703 TUTORIALS ALOUAMAR KAUSAR	14:45-15:30 DATA COMMUNICATION NETWORK THEORY ARCHANA SHARMA	15:30-16:15 DATA COMMUNICATION NETWORK THEORY ARCHANA SHARMA	16:15-16:30 Free Period

HOD

 26/05/15

Academic cell
