

**Template for Site: SITM-SAROJ INSTITUTE OF TECHNOLOGY AND MANAGEMENT, Batch: B.Tech EC 2014-18,  
Program: Electronics and Communication Engineering, Session: SEMESTER 3, Class: CLASS - 3 - 1**

DAY/TIME	1	2	3	4	5	6	7	8	9	10	11
<b>MONDAY</b>	09:00-09:45 LASER SYSTEMS AND APPLICATIONS THEORY ARCHANA SHARMA	09:45-10:30 TEST TEST	10:30-11:15 SWITCHING THEORY AND LOGIC DESIGN THEORY ARCHANA SHARMA	11:15-12:00 INDUSTRIAL PSYCHOLOGY THEORY	12:00-12:45 NETWORK ANALYSIS & SYNTHESIS THEORY SATISH CHANDRA KUSHWAHA	12:45-13:15 30 MINS LUNCH BREAK	13:15-14:00 SIGNALS AND SYSTEMS THEORY PRATYUSH PUSHKAR	14:00-14:45 NETWORK ANALYSIS & SYNTHESIS TUTORIALS SATISH CHANDRA KUSHWAHA	14:45-15:30 ELECTRONIC DEVICE LAB. VIKSI GAUR	15:30-16:15 ELECTRONIC DEVICE LAB. VIKSI GAUR	16:15-16:30 Free Period
<b>TUESDAY</b>	09:00-09:45 LASER SYSTEMS AND APPLICATIONS THEORY	09:45-10:30 FUNDAMENTA L OF ELECTRONIC DEVICES THEORY VIKSI GAUR	10:30-11:15 SWITCHING THEORY AND LOGIC DESIGN THEORY ARCHANA SHARMA	11:15-12:00 INDUSTRIAL PSYCHOLOGY THEORY	12:00-12:45 NETWORK ANALYSIS & SYNTHESIS THEORY SATISH CHANDRA KUSHWAHA	12:45-13:15 30 MINS LUNCH BREAK	13:15-14:00 SIGNALS AND SYSTEMS THEORY PRATYUSH PUSHKAR	14:00-14:45 SIGNALS AND SYSTEMS TUTORIALS PRATYUSH PUSHKAR	14:45-15:30 PCB & ELECTRONICS WORKSHOP LAB ARCHANA SHARMA	15:30-16:15 PCB & ELECTRONICS WORKSHOP LAB ARCHANA SHARMA	16:15-16:30 Free Period
<b>WEDNESDAY</b>	09:00-09:45 LASER SYSTEMS AND APPLICATIONS THEORY	09:45-10:30 FUNDAMENTA L OF ELECTRONIC DEVICES THEORY VIKSI GAUR	10:30-11:15 SWITCHING THEORY AND LOGIC DESIGN THEORY ARCHANA SHARMA	11:15-12:00 INDUSTRIAL PSYCHOLOGY THEORY	12:00-12:45 NETWORK ANALYSIS & SYNTHESIS THEORY SATISH CHANDRA KUSHWAHA	12:45-13:15 30 MINS LUNCH BREAK	13:15-14:00 SIGNALS AND SYSTEMS THEORY PRATYUSH PUSHKAR	14:00-14:45 SWITCHING THEORY AND LOGIC DESIGN TUTORIALS ARCHANA SHARMA	14:45-15:30 DIGITAL ELECTRONICS LAB ARCHANA SHARMA	15:30-16:15 DIGITAL ELECTRONICS LAB ARCHANA SHARMA	16:15-16:30 Free Period
<b>THURSDAY</b>	09:00-09:45 LASER SYSTEMS AND APPLICATIONS THEORY	09:45-10:30 FUNDAMENTA L OF ELECTRONIC DEVICES THEORY VIKSI GAUR	10:30-11:15 SWITCHING THEORY AND LOGIC DESIGN THEORY ARCHANA SHARMA	11:15-12:00 INDUSTRIAL PSYCHOLOGY THEORY	12:00-12:45 NETWORK ANALYSIS & SYNTHESIS THEORY SATISH CHANDRA KUSHWAHA	12:45-13:15 30 MINS LUNCH BREAK	13:15-14:00 SIGNALS AND SYSTEMS THEORY PRATYUSH PUSHKAR	14:00-14:45 FUNDAMENTA L OF ELECTRONIC DEVICES TUTORIALS VIKSI GAUR	14:45-15:30 NETWORK ANALYSIS & SYNTHESIS LAB SATISH CHANDRA KUSHWAHA	15:30-16:15 NETWORK ANALYSIS & SYNTHESIS LAB SATISH CHANDRA KUSHWAHA	16:15-16:30 Free Period

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*Academic cell*