

**Template for Site: SITM-SAROJ INSTITUTE OF TECHNOLOGY AND MANAGEMENT, Batch: B.Tech CS 2013-17,  
Program: Computer Science and Engineering, Session: SEMESTER 5, Class: CLASS - 5 - 1**

| DAY/TIME         | 1  | 2   | 3  | 4   | 5  | 6                                  | 7  | 8  | 9  | 10   | 11                         |
|------------------|--|---|--|---|--|------------------------------------|--|--|--|--|----------------------------|
| <b>MONDAY</b>    | 09:00-09:45<br>DESIGN & ANALYSIS OF ALGORITHM THEORY<br>SHASHANK KUMAR SINGH | 09:45-10:30<br>TEST TEST                                | 10:30-11:15<br>DATABASE MANAGEMENT SYSTEM THEORY<br>SHASHANK KUMAR SINGH | 11:15-12:00<br>WEB TECHNOLOGY THEORY<br>DISHA SINGH | 12:00-12:45<br>COMPUTER ARCHITECTURE THEORY<br>NIRNIMESH KUMAR SINGH | 12:45-13:15<br>30 MINS LUNCH BREAK | 13:15-14:00<br>ENGINEERING & MANAGERIAL ECONOMICS THEORY | 14:00-14:45<br>DATABASE MANAGEMENT SYSTEM GATE<br>SHASHANK KUMAR SINGH       | 14:45-15:30<br>DESIGN AND ANALYSIS LAB<br>SHASHANK KUMAR SINGH | 15:30-16:15<br>DESIGN AND ANALYSIS LAB<br>SHASHANK KUMAR SINGH | 16:15-16:30<br>Free Period |
| <b>TUESDAY</b>   | 09:00-09:45<br>DESIGN & ANALYSIS OF ALGORITHM THEORY<br>SHASHANK KUMAR SINGH | 09:45-10:30<br>PRINCIPAL OF PROGRAMMING LANGUAGE THEORY | 10:30-11:15<br>DATABASE MANAGEMENT SYSTEM THEORY<br>SHASHANK KUMAR SINGH | 11:15-12:00<br>WEB TECHNOLOGY THEORY<br>DISHA SINGH | 12:00-12:45<br>COMPUTER ARCHITECTURE THEORY<br>NIRNIMESH KUMAR SINGH | 12:45-13:15<br>30 MINS LUNCH BREAK | 13:15-14:00<br>ENGINEERING & MANAGERIAL ECONOMICS THEORY | 14:00-14:45<br>DATABASE MANAGEMENT SYSTEMS TUTORIALS<br>SHASHANK KUMAR SINGH | 14:45-15:30<br>DBMS LAB<br>SHASHANK KUMAR SINGH                | 15:30-16:15<br>DBMS LAB<br>SHASHANK KUMAR SINGH                | 16:15-16:30<br>Free Period |
| <b>WEDNESDAY</b> | 09:00-09:45<br>DESIGN & ANALYSIS OF ALGORITHM THEORY<br>SHASHANK KUMAR SINGH | 09:45-10:30<br>PRINCIPAL OF PROGRAMMING LANGUAGE THEORY | 10:30-11:15<br>DATABASE MANAGEMENT SYSTEM THEORY<br>SHASHANK KUMAR SINGH | 11:15-12:00<br>WEB TECHNOLOGY THEORY<br>DISHA SINGH | 12:00-12:45<br>COMPUTER ARCHITECTURE THEORY<br>NIRNIMESH KUMAR SINGH | 12:45-13:15<br>30 MINS LUNCH BREAK | 13:15-14:00<br>ENGINEERING & MANAGERIAL ECONOMICS THEORY | 14:00-14:45<br>PRINCIPAL OF PROGRAMMING LANGUAGE THEORY                      | 14:45-15:30<br>WEB TECHNOLOGY LAB<br>DISHA SINGH               | 15:30-16:15<br>WEB TECHNOLOGY LAB<br>DISHA SINGH               | 16:15-16:30<br>Free Period |
| <b>THURSDAY</b>  | 09:00-09:45<br>DESIGN & ANALYSIS OF ALGORITHM THEORY<br>SHASHANK KUMAR SINGH | 09:45-10:30<br>PRINCIPAL OF PROGRAMMING LANGUAGE THEORY | 10:30-11:15<br>DATABASE MANAGEMENT SYSTEM THEORY<br>SHASHANK KUMAR SINGH | 11:15-12:00<br>WEB TECHNOLOGY THEORY<br>DISHA SINGH | 12:00-12:45<br>COMPUTER ARCHITECTURE THEORY<br>NIRNIMESH KUMAR SINGH | 12:45-13:15<br>30 MINS LUNCH BREAK | 13:15-14:00<br>ENGINEERING & MANAGERIAL ECONOMICS THEORY | 14:00-14:45<br>PRINCIPAL OF PROGRAMMING LANGUAGE TUTORIALS                   | 14:45-15:30<br>PPL LAB<br>LAB                                  | 15:30-16:15<br>PPL LAB<br>LAB                                  | 16:15-16:30<br>Free Period |

Academic cell

HOD