

Template for Site: SITM-SAROJ INSTITUTE OF TECHNOLOGY AND MANAGEMENT , Batch: B.Tech BM 2014-18 ,

Program: Biomedical Engineering , Session: SEMESTER 3 , Class: CLASS - 3 - 1

| DAY/TIME | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 |
|------------------|---|---|---|--|--|---------------------------------------|--|--|---|--|----------------------------|
| MONDAY | 09:00-09:45 FUNDAMENTA L OF ELECTRONIC DEVICES THEORY VIKSIT GAUR | 09:45-10:30 TEST TEST | 10:30-11:15 SWITCHING THEORY AND LOGIC DESIGN THEORY ARCHANA SHARMA | 11:15-12:00 INDUSTRIAL PSYCHOLOGY THEORY | 12:00-12:45 NETWORK ANALYSIS & SYNTHESIS THEORY SATISH CHANDRA KUSHWAHA | 12:45-13:15 30 MINS LUNCH BREAK | 13:15-14:00 SIGNALS AND SYSTEMS THEORY PRATYUSH PUSHKAR | 14:00-14:45 FUNDAMENTA L OF ELECTRONIC DEVICES TUTORIALS VIKSIT GAUR | 14:45-15:30 ELECTRONIC DEVICE LAB. LAB VIKSIT GAUR | 15:30-16:15 ELECTRONIC DEVICE LAB. LAB VIKSIT GAUR | 16:15-16:30 Free Period |
| TUESDAY | 09:00-09:45 FUNDAMENTA L OF ELECTRONIC DEVICES THEORY VIKSIT GAUR | 09:45-10:30 LASER SYSTEMS AND APPLICATIONS THEORY | 10:30-11:15 SWITCHING THEORY AND LOGIC DESIGN THEORY ARCHANA SHARMA | 11:15-12:00 INDUSTRIAL PSYCHOLOGY THEORY | 12:00-12:45 NETWORK ANALYSIS & SYNTHESIS THEORY SATISH CHANDRA KUSHWAHA | 12:45-13:15 30 MINS LUNCH BREAK | 13:15-14:00 SIGNALS AND SYSTEMS THEORY PRATYUSH PUSHKAR | 14:00-14:45 SIGNALS AND SYSTEMS TUTORIALS PRATYUSH PUSHKAR | 14:45-15:30 PCB & ELECTRONICS WORKSHOP LAB LAB ARCHANA SHARMA | 15:30-16:15 PCB & ELECTRONICS WORKSHOP LAB LAB ARCHANA SHARMA | 16:15-16:30 Free Period |
| WEDNESDAY | 09:00-09:45 FUNDAMENTA L OF ELECTRONIC DEVICES THEORY VIKSIT GAUR | 09:45-10:30 LASER SYSTEMS AND APPLICATIONS THEORY | 10:30-11:15 SWITCHING THEORY AND LOGIC DESIGN THEORY ARCHANA SHARMA | 11:15-12:00 INDUSTRIAL PSYCHOLOGY THEORY | 12:00-12:45 NETWORK ANALYSIS & SYNTHESIS THEORY | 12:45-13:15 30 MINS LUNCH BREAK | 13:15-14:00 SIGNALS AND SYSTEMS THEORY PRATYUSH PUSHKAR | 14:00-14:45 DIGITAL LOGIC DESIGN TUTORIALS | 14:45-15:30 DIGITAL LOGIC DESIGN LAB ARCHANA SHARMA | 15:30-16:15 DIGITAL LOGIC DESIGN LAB ARCHANA SHARMA | 16:15-16:30 Free Period |
| THURSDAY | 09:00-09:45 FUNDAMENTA L OF ELECTRONIC DEVICES THEORY VIKSIT GAUR | 09:45-10:30 LASER SYSTEMS AND APPLICATIONS THEORY | 10:30-11:15 SWITCHING THEORY AND LOGIC DESIGN THEORY | 11:15-12:00 SIGNALS AND SYSTEMS THEORY PRATYUSH PUSHKAR | 12:00-12:45 NETWORK ANALYSIS & SYNTHESIS THEORY | 12:45-13:15 30 MINS LUNCH BREAK | 13:15-14:00 INDUSTRIAL PSYCHOLOGY THEORY | 14:00-14:45 FUNDAMENTA L OF ELECTRONIC DEVICES TUTORIALS VIKSIT GAUR | 14:45-15:30 NETWORK ANALYSIS & SYNTHESIS LAB SATISH CHANDRA KUSHWAHA | 15:30-16:15 NETWORK ANALYSIS & SYNTHESIS THEORY | 16:15-16:30 Free Period |

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for
SITM
26/09/18

Academic cell
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